

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10552948			
<b>Filing Date:</b>	11-Sep-2006			
<b>Title of Invention:</b>	Read and erase verify methods and circuits suitable for low voltage non-volatile memories			
First Named Inventor/Applicant Name:	Jian Chen			
<b>Filer:</b>	Michael G. Cleveland/Eileen Bowen (MGC)			
<b>Attorney Docket Number:</b>	SNDK.284US1			
Filed as Large Entity				
<b>U.S. National Stage under 35 USC 371 Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Post-Allowance-and-Post-Issuance:				
<b>Extension-of-Time:</b>				
Extension - 3 months with \$0 paid	1253	1	1050	1050

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Submission- Information Disclosure Stmt	1806	1	180	180
Total in USD (\$)				1230